KOPIN

CyberDisplay® VGA LVS

FEATURES

- 640 x 480 x 3 dot active resolution (921,600 dots)
- 3.75 um (W) x 11.25 (H) um dot pitch
- Ultra-compact (0.35" diagonal)
- Active pixel area (7.2 mm x 5.4 mm)
- Parallel RGB analog input
- Simple interface for CMOS compatible driver chip
- Power-saving sleep mode
- Integrated low-voltage detect
- Integrated horizontal and vertical scanners
- Bi-directional horizontal and vertical scanning

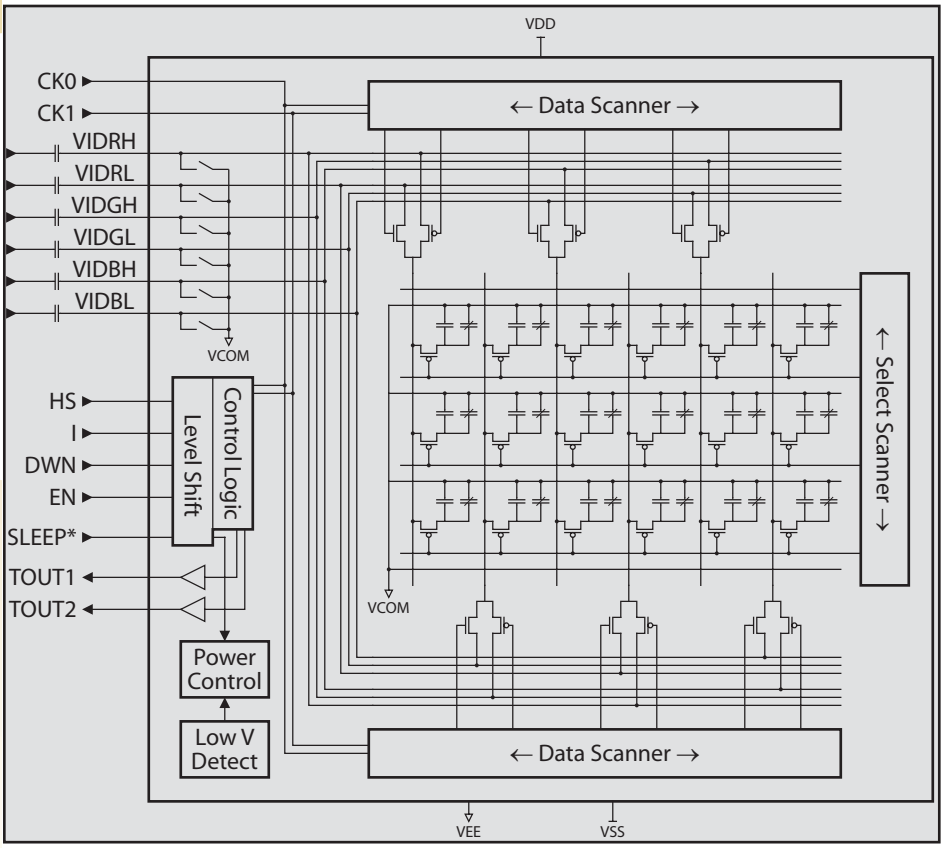
The CyberDisplay® VGA LVS is a color-filter active matrix liquid crystal display (AMLCD) with a resolution of 640x480. The CyberDisplay VGA LVS utilizes high-preformance single crystal silicon transistors. The transmissive CyberDisplay VGA LVS has the same display architecture as the industry standard LCD monitor or TV. The ultra-compact CyberDisplay VGA LVS is ideal for compact consumer or professional portable devices.

Functional Description

The CyberDisplay VGA LVS features Kopin's low-voltage architecture for low power consumption and compatibility with CMOS driver ICs. Bidirectional horizontal and vertical scanner circuits are integrated along with a sleep mode. The total dot active resolution is 640 x 480 x 3 (921,600 dots).

The CyberDisplay VGA LVS can be driven by the A912 or A913 controller IC.

BLOCK DIAGRAM



*Specifications subject to change without notice

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CyberDisplay ® VGA LVS with a spatial resolution of 640 x 480 is ideal for compact consumer or professional portable devices.

INTERFACE PIN LIST

CyberDisplay® VGA LVS

PIN	SYMBOL	DESCRIPTION
1	VCOM	Pixel common electrode
2	VEE	Supply = 0V
3	VDD	Supply
4	VSS	Supply
5	CK0	Clock
6	CK1	Clock
7	I	Instruction input
8	HS	Horizontal start
9	DWN	Vertical scan control
10	EN	Row enable
11	SLEEP*	Sleep mode
12	VIDRH	High red video input
13	VIDGH	High green video input
14	VIDBH	High blue video input
15	VIDRL	Low red video input
16	VIDGL	Low green video input
17	VIDBL	Low blue video input
18	TOUT1	Test output
19	TOUT2	Test output
20	VCOM	Pixel common electrode
21	RESV	Reserved (Tied to Pin 20)
22	RESV	Reserved (Tied to Pin 20)
23	RESV	Reserved (Tied to Pin 20)

*Signal is active low